

FIG. 1 is a block diagram of a computer system 100. The system 100 includes a processor 102, a design module 106, an RDM module 108, a bus adapter 114, and a database 128. The processor 102, design module 106, and RDM module 108 are connected to a processor/memory bus 116. The bus adapter 114 is connected to the processor/memory bus 116 and an input/output bus 118. The input/output bus 118 is connected to four I/O controllers 120. Each I/O controller 120 is connected to a specific device: a network interface 122, a mass storage device 124, a monitor 126, and a database 128. The network interface 122 is also connected to a network, with signals to and from the network.

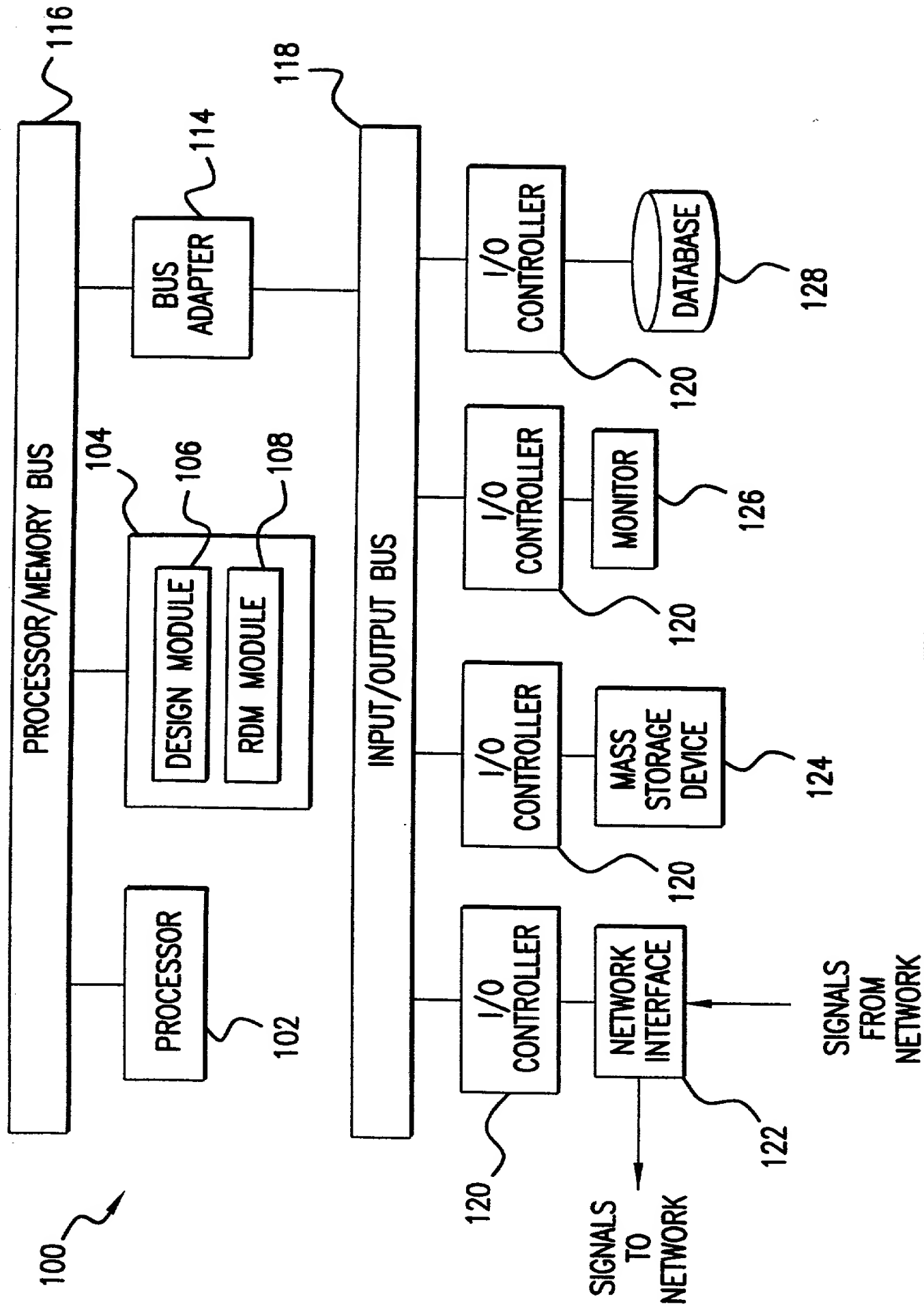


FIG.1

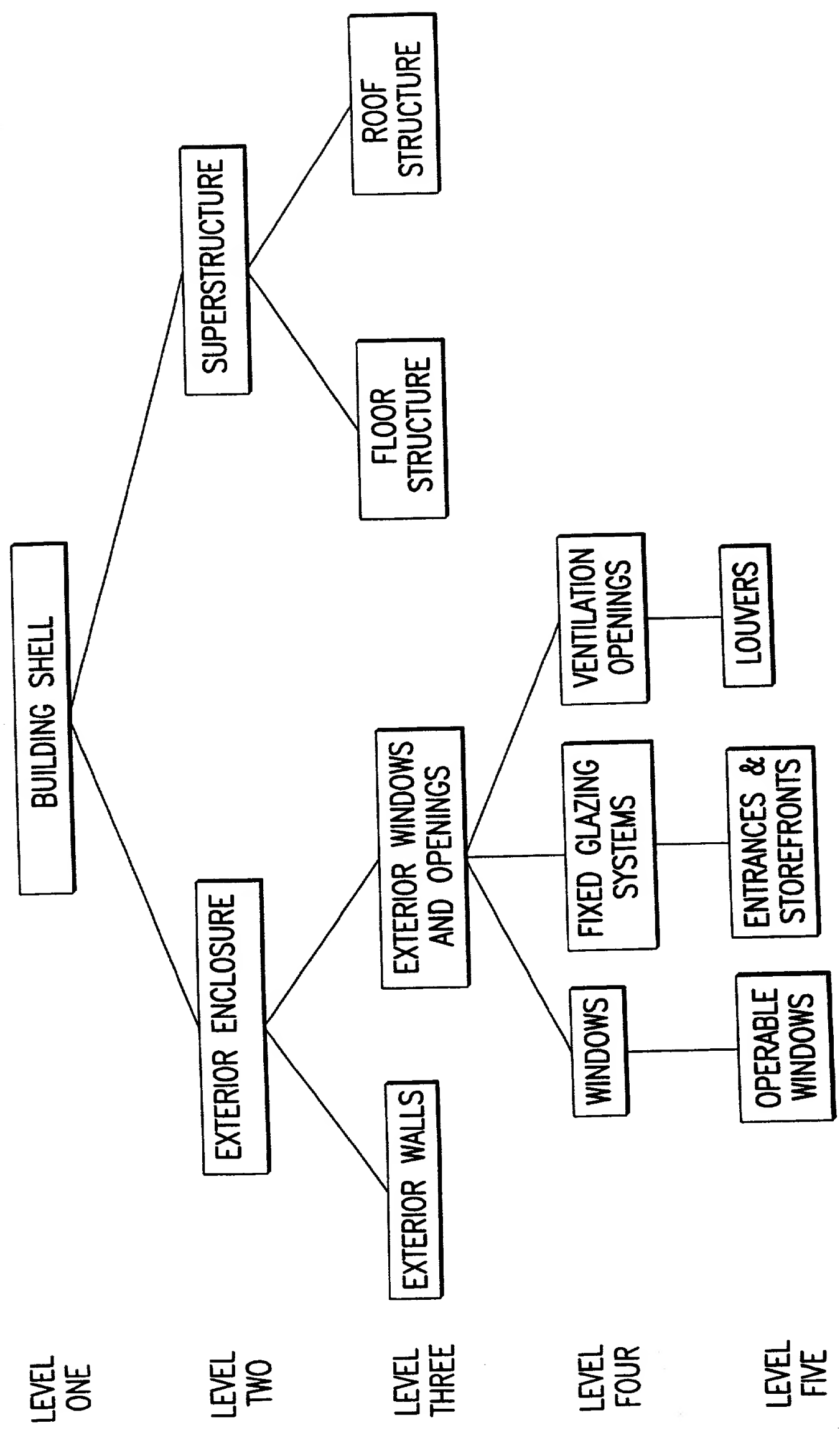


FIG.2

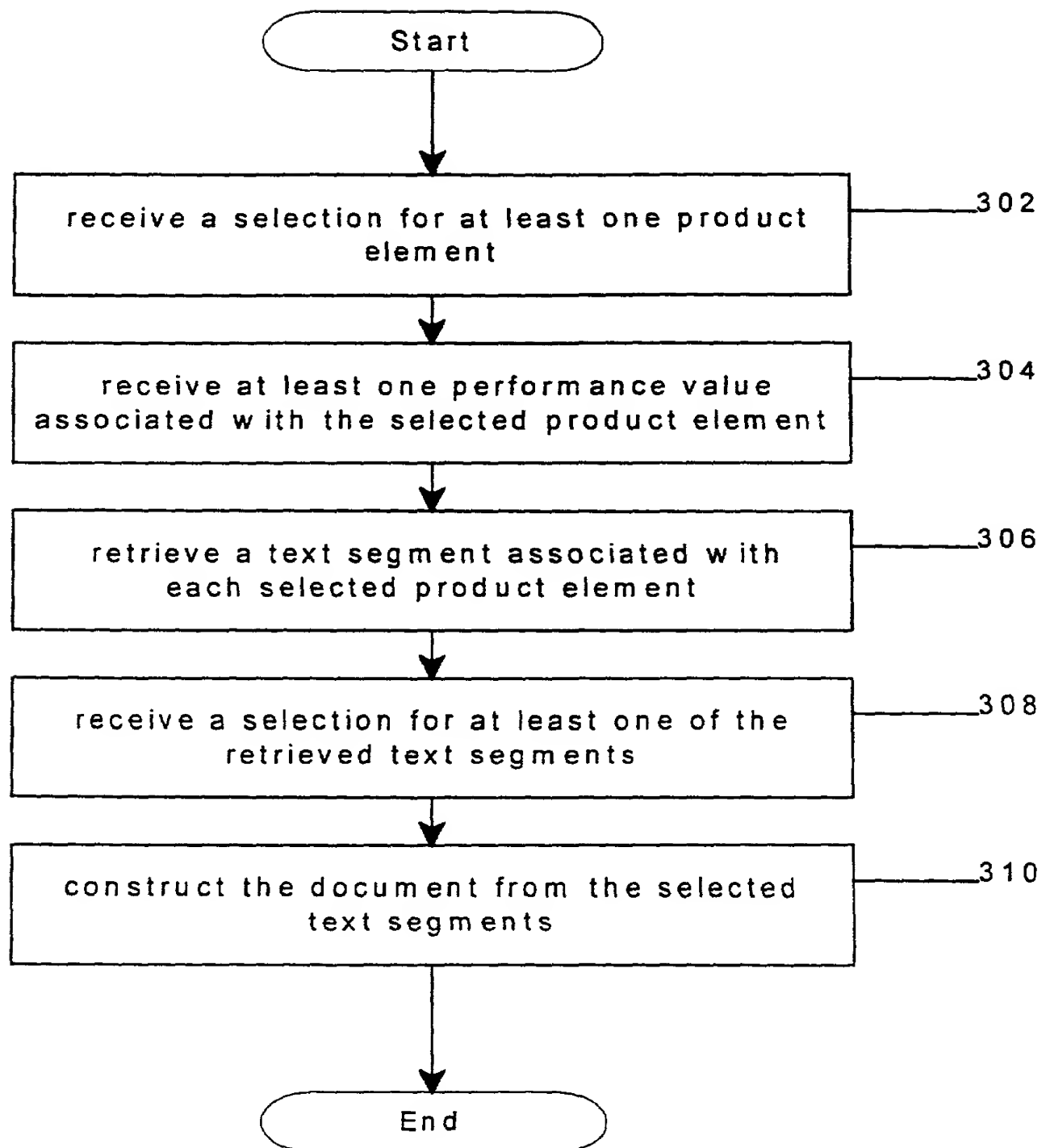


FIG. 3

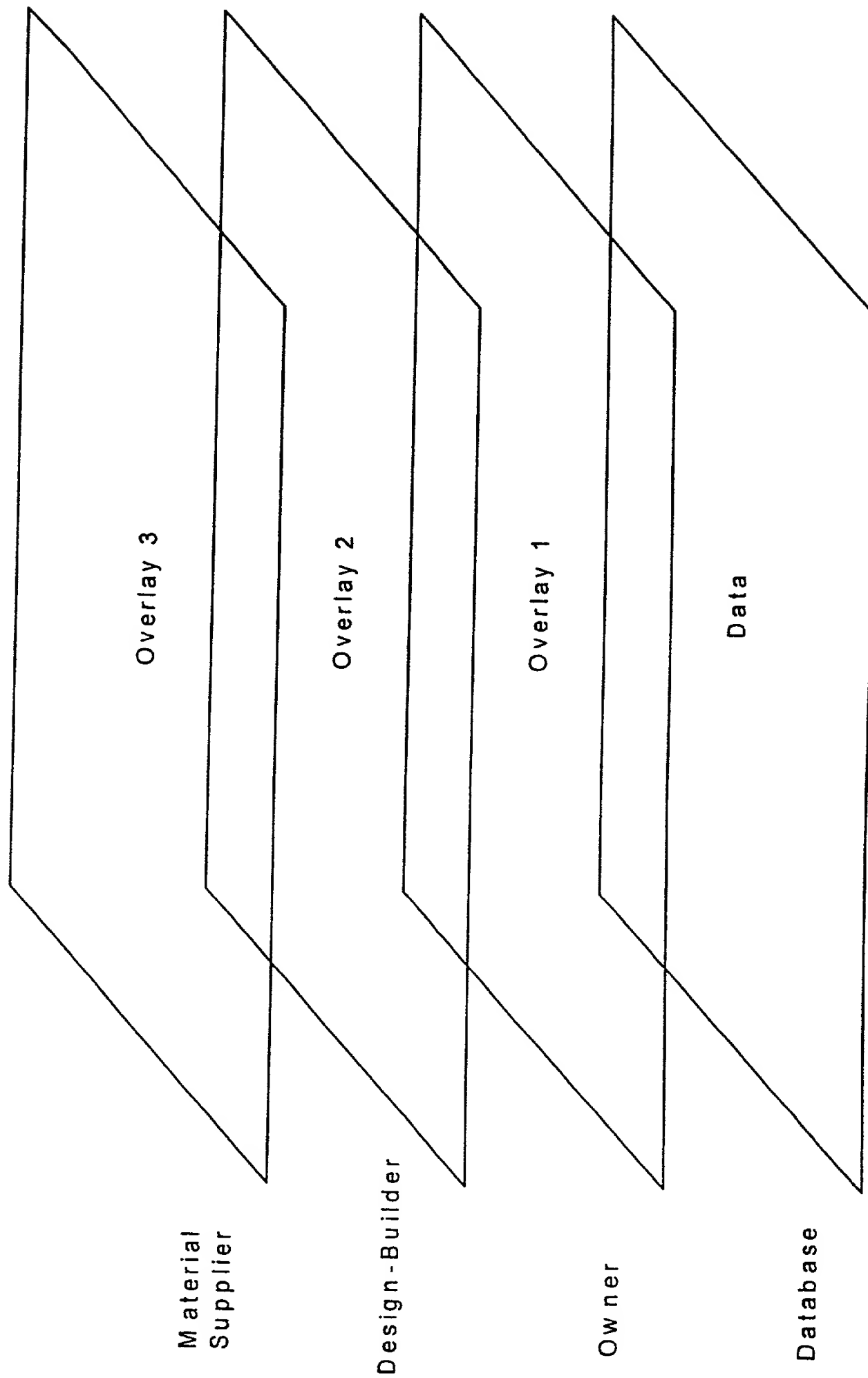


FIG. 4

500

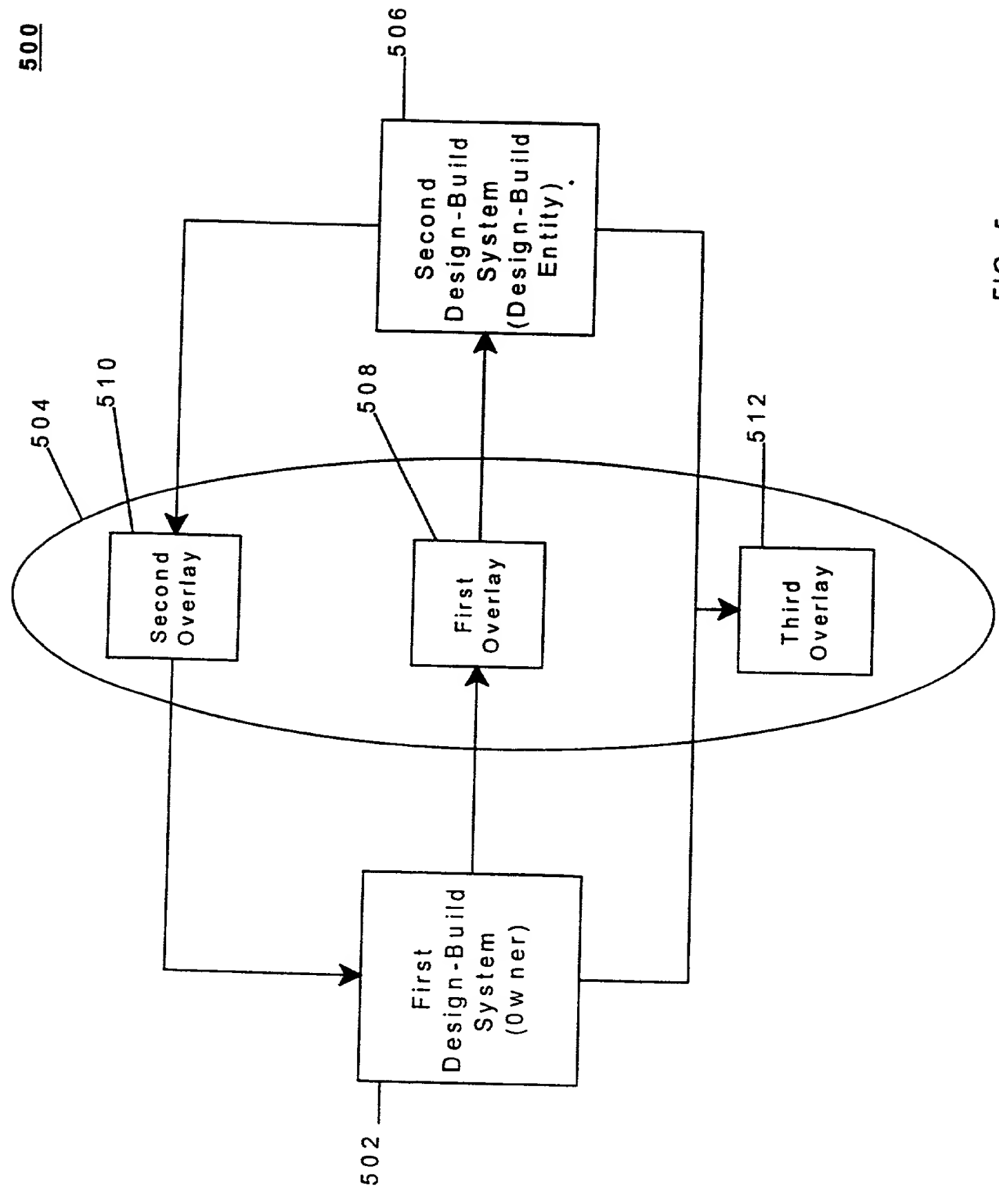


FIG. 5

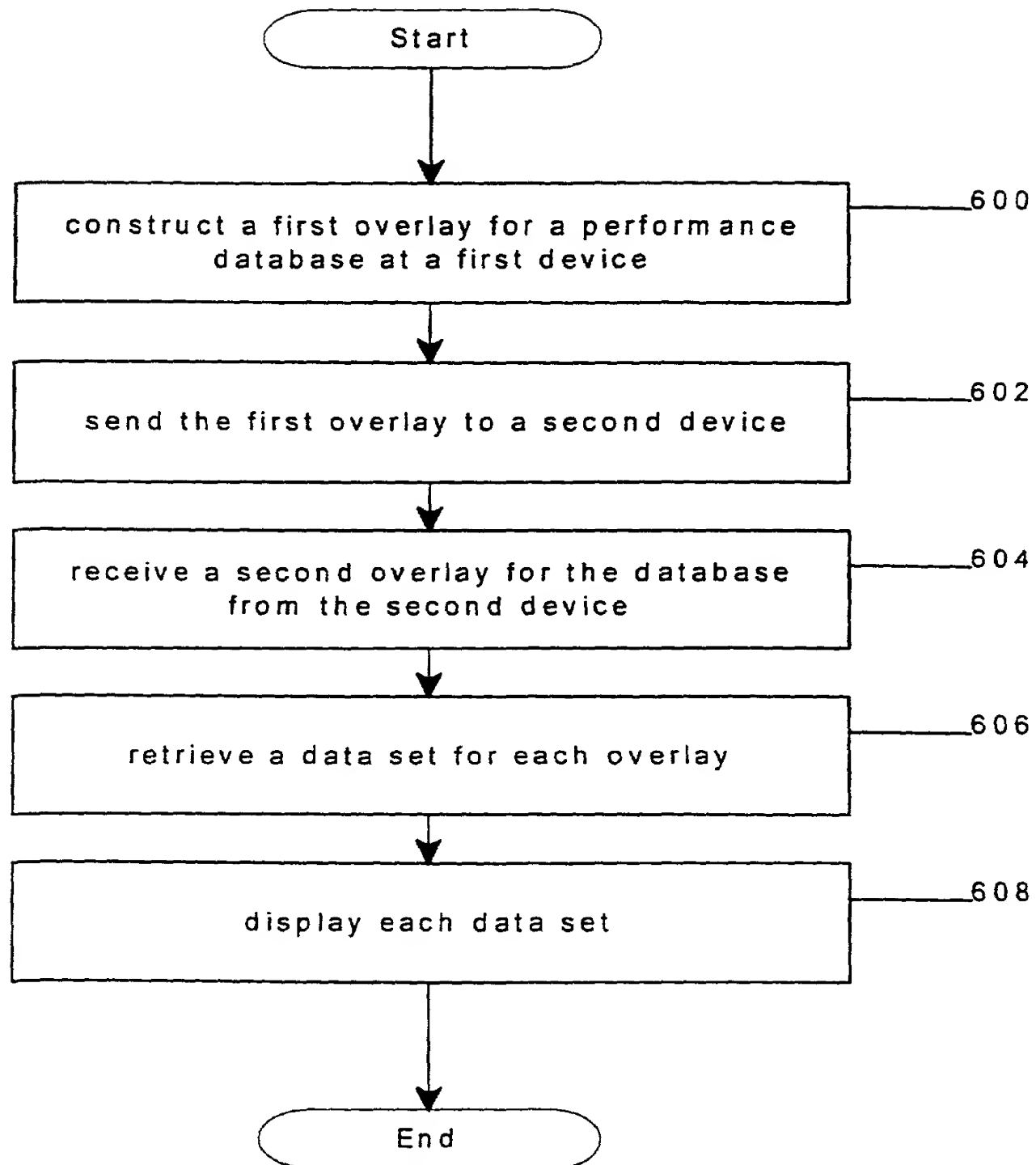


FIG. 6

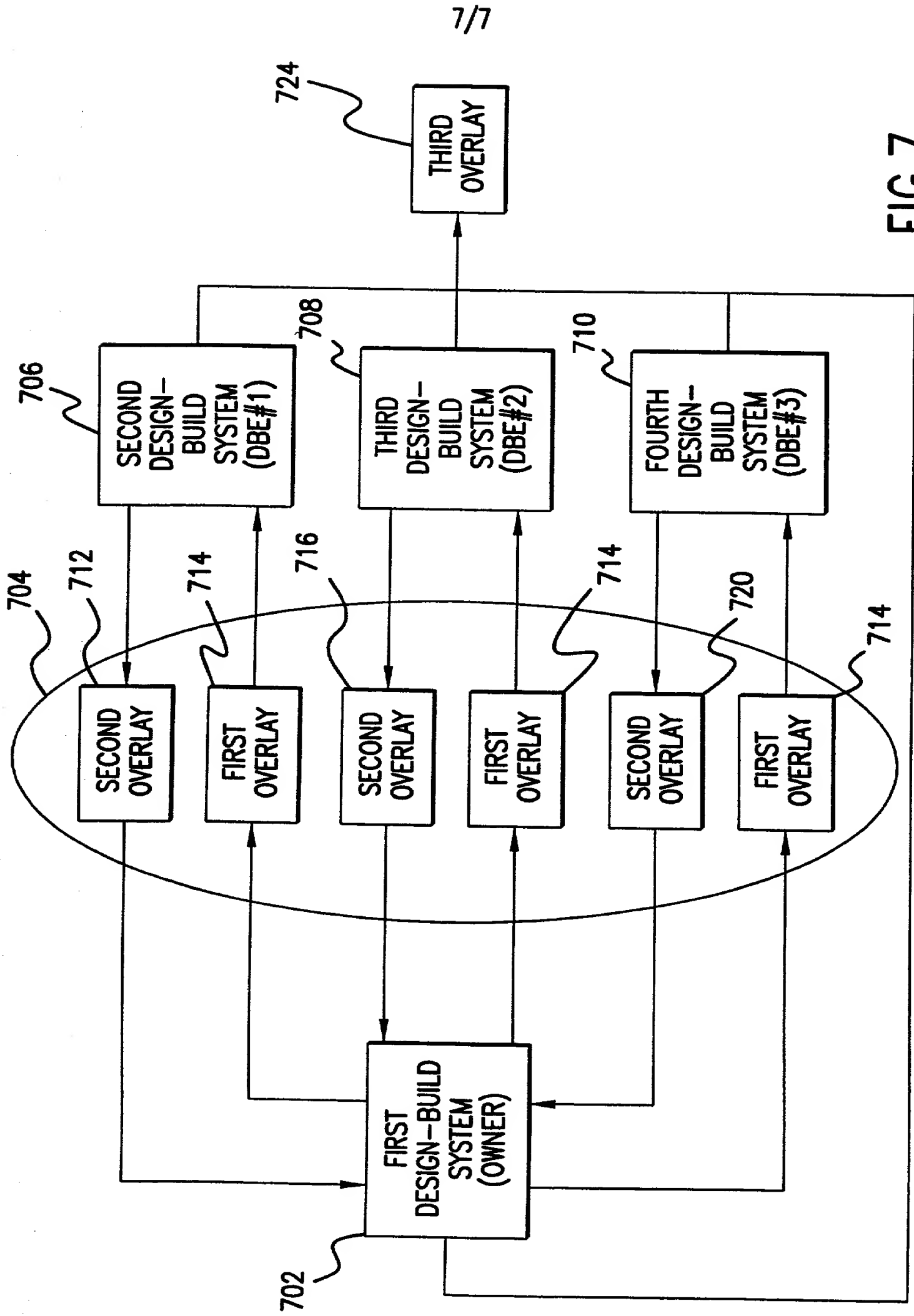


FIG. 7